

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION**

App./Confirm Nos.: 09/683,985 2597

Applicant: Steven Voldman

Filing Date: March 8, 2002

Title: **A METHOD OF FORMING LOW  
CAPACITANCE ESD ROBUST DIODES**

Atty. Docket: BUR920020014US1

Today's Date: November 16, 2007

Examiner: Ori Nadav

Group Art Unit: 2811

**Amendments Under 37 C.F.R 1.114**

**Amendment H**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Final Office Action dated October 4, 2007, Applicant respectfully requests reconsideration of the outstanding rejections and reexamination of the present application in light of the following remarks and amendments.